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Total number of pages in this submission including transmittal

Application Number	10/786,010
Filing Date	02/26/2004
First Named Inventor	Pierte ROO
Group Art Unit	Unknown
Examiner Name	Unknown
Attorney Docket Number	MP0039.C1

4

ENCLOSURES (check all that apply)

<input type="checkbox"/> Fee Transmittal Form	<input type="checkbox"/> Assignment Papers <i>(for an Application)</i>	<input type="checkbox"/> After Allowance Communication to TC
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<input type="checkbox"/> Certified Copy of Priority Document(s)	<input type="checkbox"/> CD, Number of CD(s) _____	
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REMARKS		

SIGNATURE OF APPLICANT, ATTORNEY, OR AGENT

Firm Name	KATTEN MUCHIN ROSENMAN, LLP		
Signature			
Printed Name	Andrew J. Bateman		
Reg. No.	45,573	Date:	07/25/2005

CERTIFICATE OF FACSIMILE TRANSMISSION OR MAILING

I hereby certify that this correspondence is being facsimile transmitted to the USPTO via fax no. (571) 273-8300 or is being deposited with the US Postal Service with sufficient postage as first class mail in an envelope addressed to: Commissioner for Patents, P.O. Box 1450, Alexandria, VA 22313-1450 on the date shown below:

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Patent

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re Application of:)
Pierte ROO) Examiner: Unknown
Application No.: 10/786,010) Group Art Unit: Unknown
Filed: February 26, 2004) Confirmation No.: Unknown
For: ACTIVE RESISTIVE SUMMER FOR A) Date: July 25, 2005
TRANSFORMER HYBRID)

INFORMATION DISCLOSURE STATEMENT

Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

Sir:

In compliance with the duty of disclosure under 37 CFR § 1.56 and the requirements of M.P.E.P. § 2001.06(c), and in accordance with the practice under 37 CFR §§ 1.97 and 1.98, the Examiner's attention is directed to the documents listed on the enclosed PTO-1449s and to copies of any literature and non-U.S. patent documents submitted herewith. It is respectfully noted that Applicants do not have copies of certain literature and non-U.S. patent documents listed on the enclosed PTO-1449 forms. Applicants will provide copies of such to the Patent Office as soon as possible. If the Examiner has not received such copies at the time of consideration of this IDS, the Examiner is respectfully requested to contact the Applicants' undersigned attorney.

In accordance with 37 CFR § 1.97(h), this Information Disclosure Statement is not to be construed as an admission that the information cited is or is considered to be material to patentability as defined in 37 CFR § 1.56(b), nor as an admission that the information constitutes prior art within the meaning of 35 USC §§ 102 and/or 103.

It is respectfully requested that the information listed on the PTO-1449 be considered by the Examiner, and that an initialed copy of the PTO-1449 be returned indicating that such information was considered.

No fee is necessary for the submission of this Information Disclosure Statement.

Should the Examiner have any questions, Applicants' undersigned attorney is reachable by telephone in our Washington, D.C. office at (202) 625-3547. The correspondence address of record is provided below.

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Respectfully submitted,
KATTEN MUCHIN ROSENMAN, LLP

By: Andrew J. Bateman
Andrew J. Bateman
Attorney for Applicants
Registration No. 45,573



FORM PTO-1449 MODIFIED
U.S. PATENT AND TRADEMARK OFFICE

LIST OF REFERENCES CITED BY APPLICANT

DATE SUBMITTED TO USPTO: July 25, 2005

ATTORNEY DOCKET NO.	APPLICATION NO.
MP0039.C1	10/786,010
APPLICANT	
Pierte ROO	
FILING DATE	GROUP
02/26/2004	Unknown

FOREIGN PATENT DOCUMENTS

*EXAMINER INITIALS	DOCUMENT NUMBER	DATE	COUNTRY	CLASS	SUBCLASS	TRANSLATION OR ABSTRACT

OTHER DOCUMENTS (Including author, title, date, pertinent pages, etc.)

	LIBERALI ET AL., "Progress in High-Speed and High -Resolution CMOS Data Converters", September 12-14, 1995, pages 19-28
	SEDRA et al., "Micro-Electronic Circuits", 1982, pages 95-97 and 243-247
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	SU ET AL., "A CMOS Oversampling D/A Converter with a Current-Mode Semidigital Reconstruction Filter", December 1993, pages 1224-1233
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	MIKI ET AL., "An 80-MHz 8-bit CMOS D/A Converter", December 1986, pages 983-988
	LETHAM ET AL., "A high-performance CMOS 70-Mhzpalette/DAC", December 1987, pages 1041-1047
	NAKAMURA ET AL., "A 10-b 70-MS/s CMOS D/A converter", April 1991, pages 637-642
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	FOURNIER ET AL., "A 130-MHz 8-b CMOS video DAC for HDTV applications", July 1991, pages 1073-1077
	REYNOLDS, "A 320 MHz CMOS triple 8b DAC with on-chip PLL and hardware cursor", February 1994, pages 50-51
	CHIN ET AL., "A 10-b 125 MHz CMOS digital-to-analog (DAC) with threshold-voltage compensated current sources", November 1994, pages 1374-1380
	WU ET AL., "A low glitch 10-bit 75-MHz CMOS video D/A converter, January 1995, pages 68-72
	The Authoritative Dictionary of IEEE Standards Stems 7th Edition, page 280
	GRAY ET AL., "Analysis and Design of Analog Integrated Circuits, 1997

EXAMINER	DATE CONSIDERED

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FORM PTO 1449 MODIFIED U.S. PATENT AND TRADEMARK OFFICE				ATTORNEY DOCKET NO.	APPLICATION NO.		
				MP0039.C1	10/786,010		
LIST OF REFERENCES CITED BY APPLICANT				APPLICANT			
				Pierte ROO		GROUP	
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				02/26/2004	Unknown		
U.S. PATENT DOCUMENTS							
*EXAMINER INITIALS		DOCUMENT NUMBER	DATE	NAME	CLASS	SUBCLASS	FILING DATE
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		5,585,802	12/17/1996	Cabler et al.			
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		6,535,987 B1	03/18/2003	Ferrant			
		6,633,178 B2	10/14/2003	Wilcox et al.			
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